

Fig. 1

Fig. 2

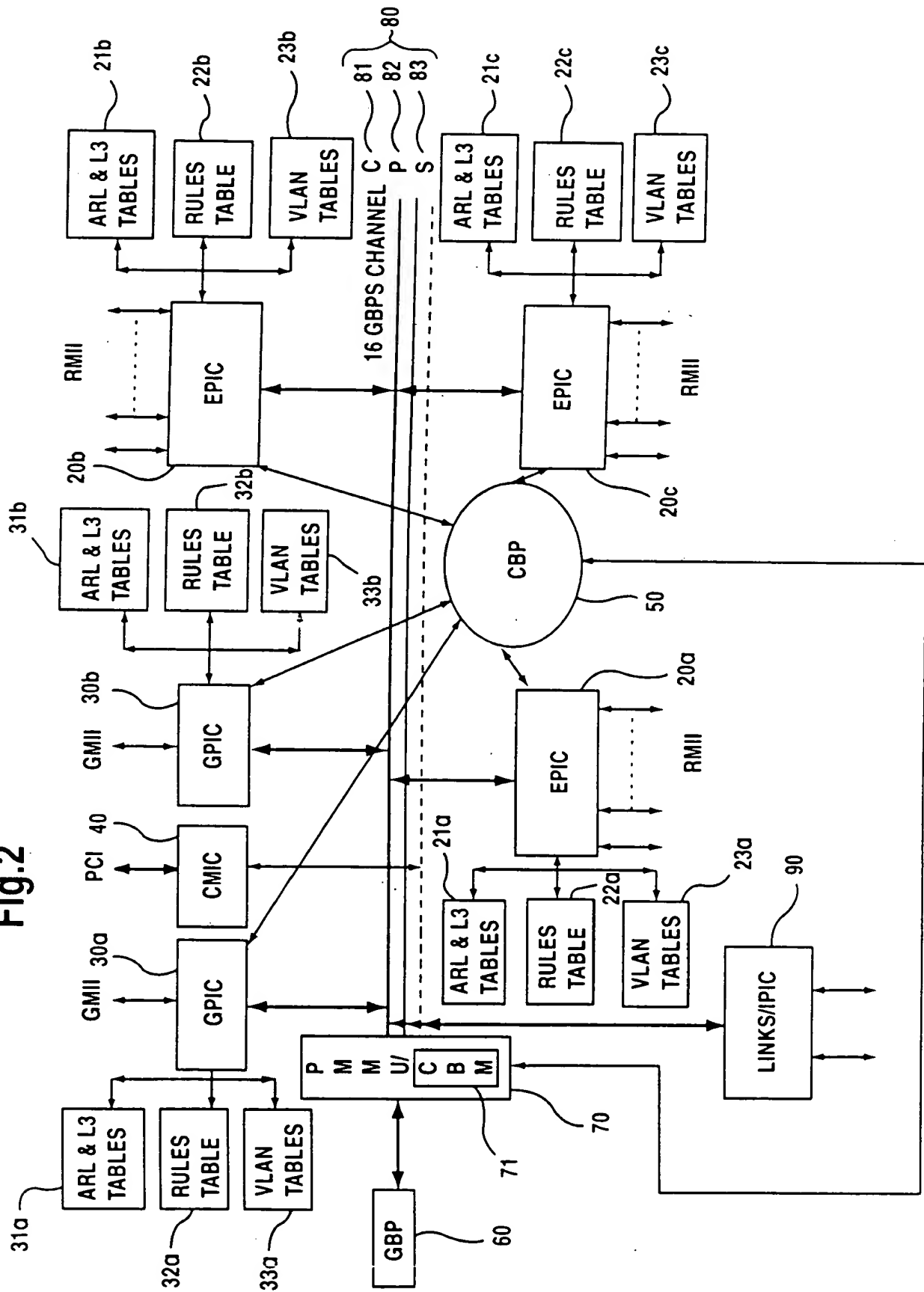


Fig.3

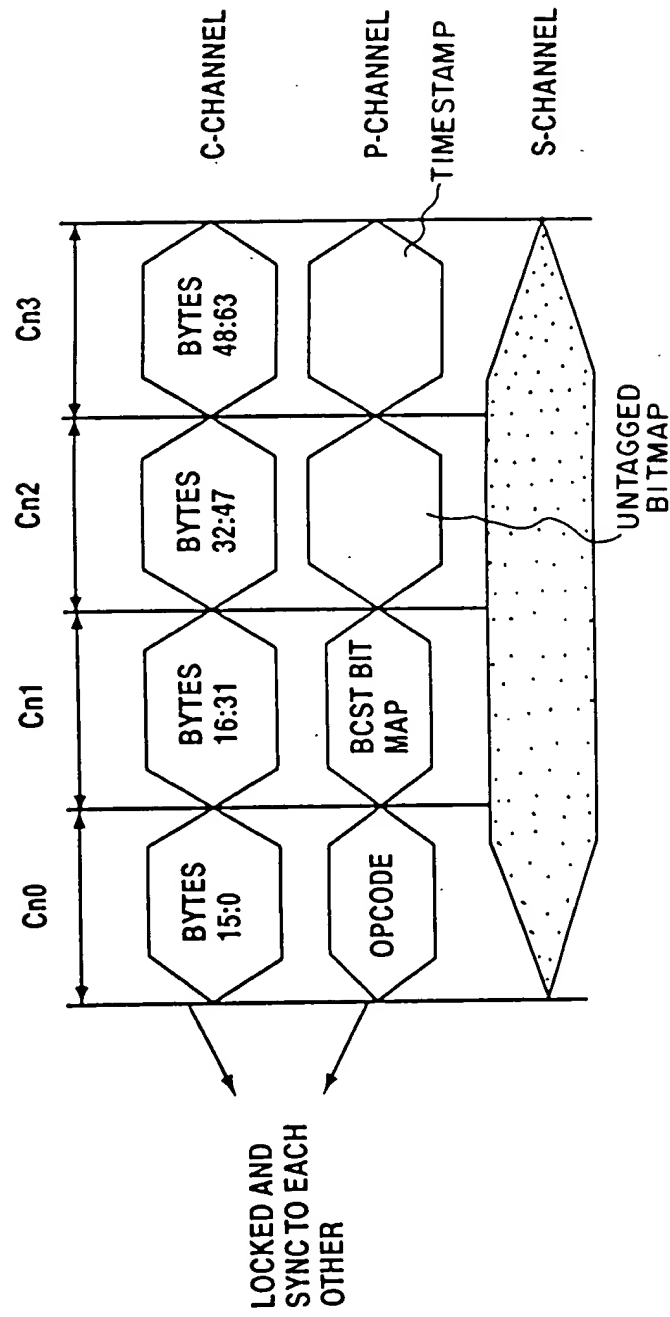
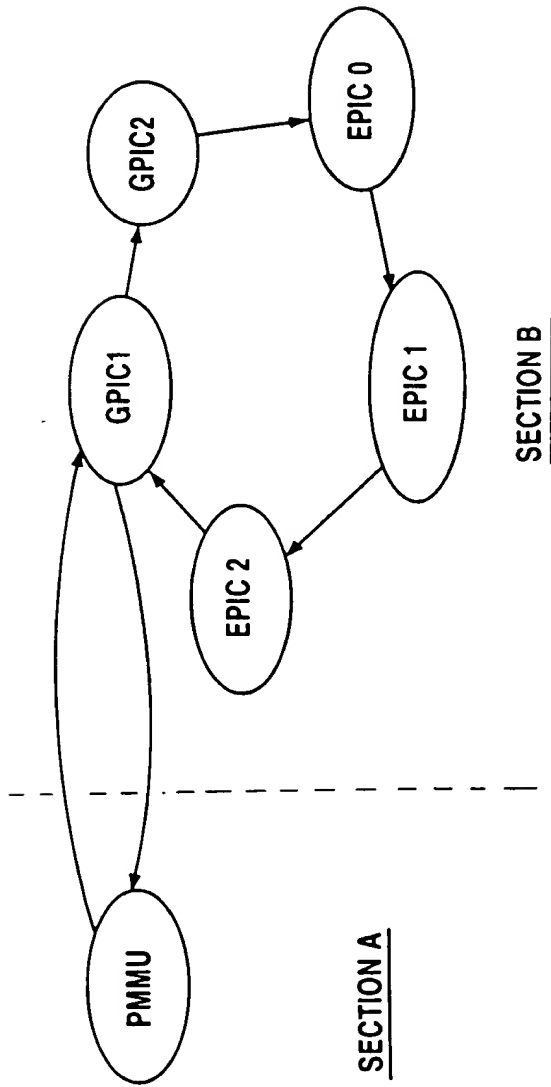


Fig.4a



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Fig.4b

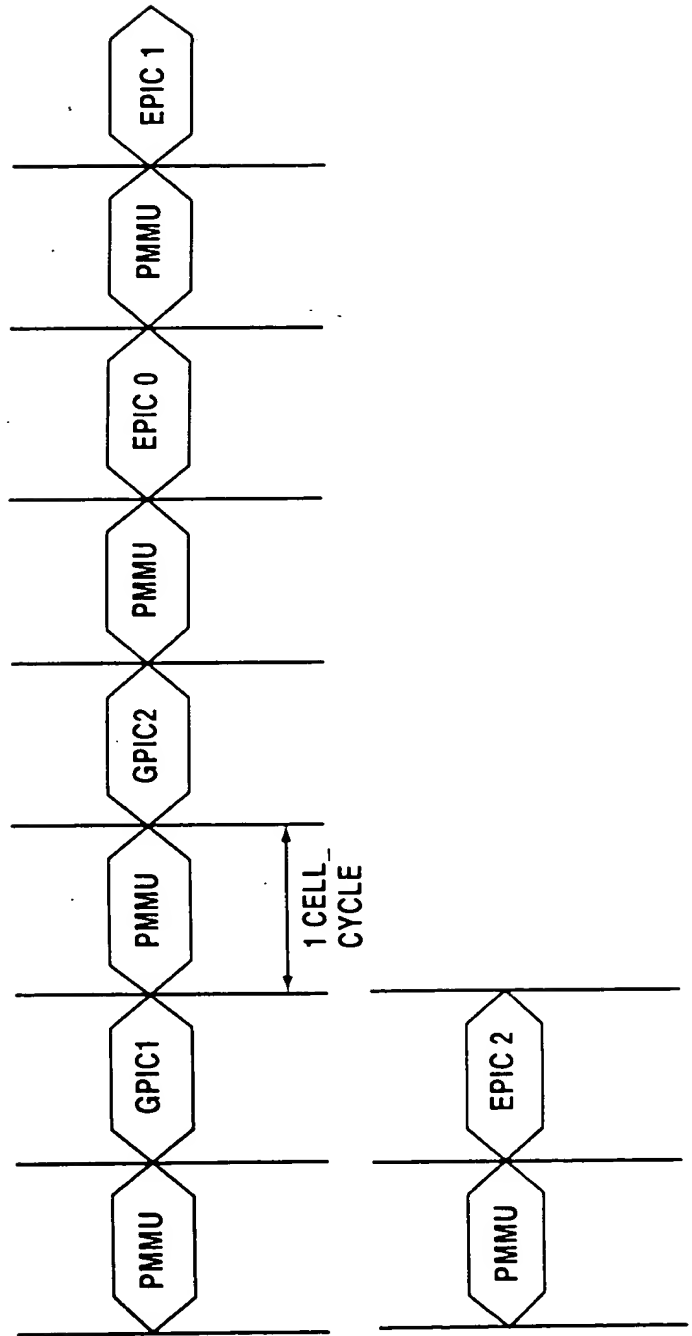


Fig.5

PROTOCOL CHANNEL MESSAGES

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
OP CODE	I	RESERVED	NXT CELL	SRC DEST PORT				COS	J	S	E	CR	P	O	LEN
	P														
	X														

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
RESERVED		BC/MC PORTBITMAP													

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
U	RES	UNTAGGED PORTBITMAP/SRC PORT NUMBER (BIT0..5)													

30	28	26	24	22	20	18	16	14	12	10	8	6	4	2	0
CPU OPCODES										TIME STAMP					

PRIOR ART

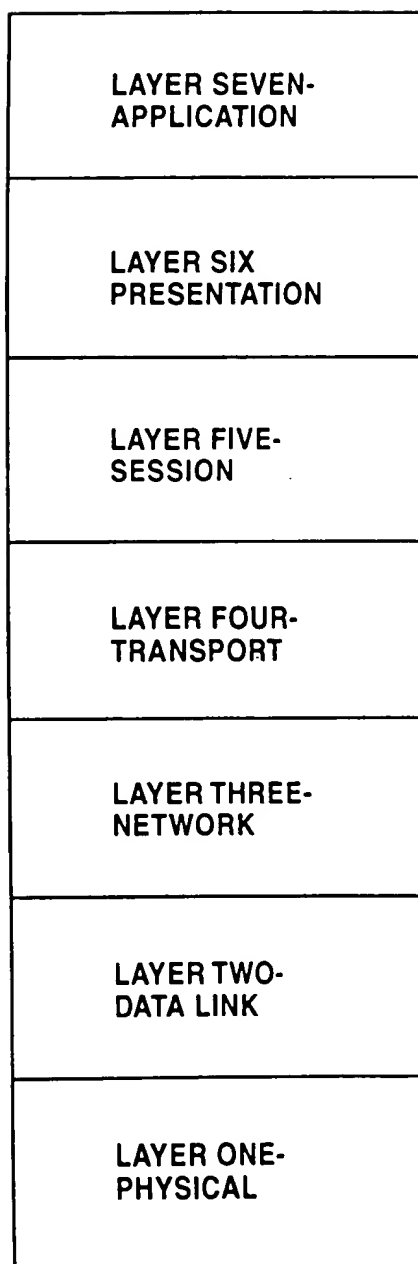


Fig.9

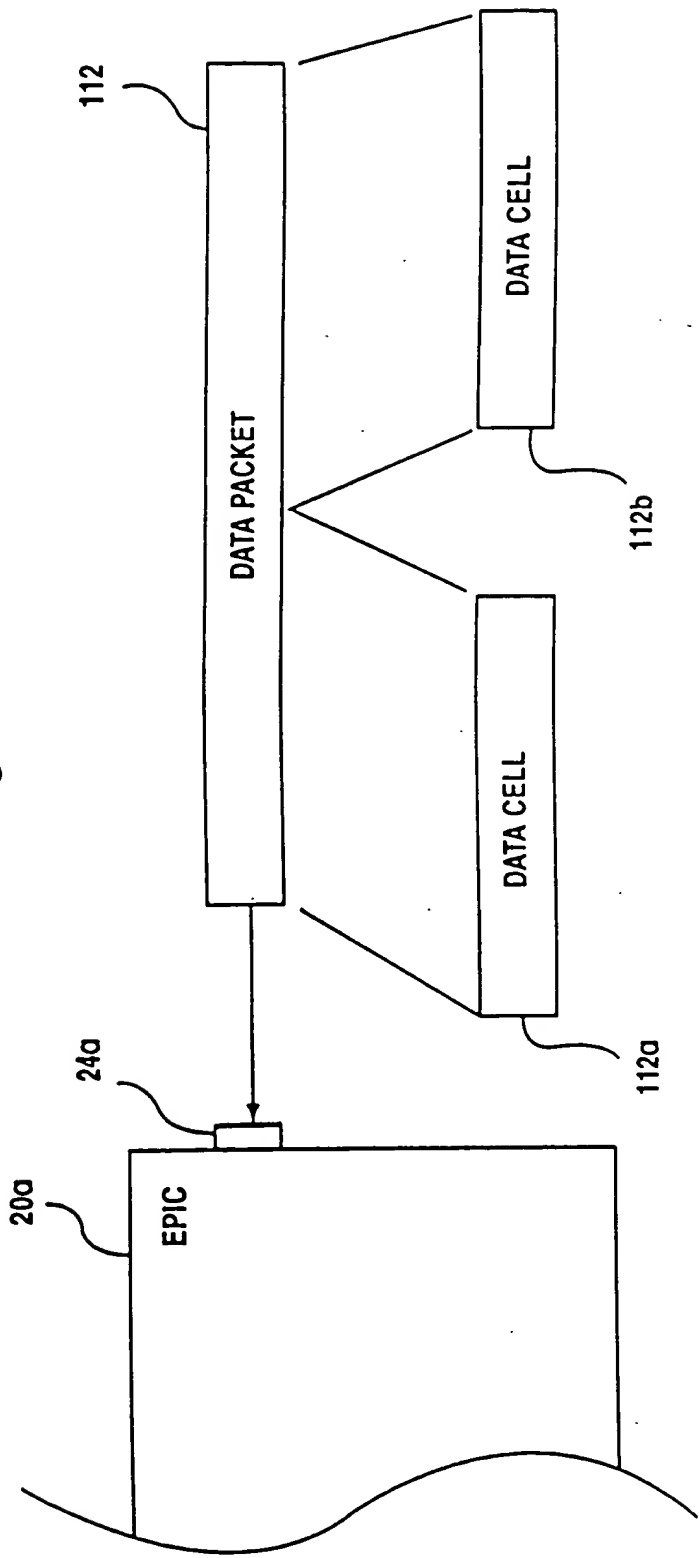


Fig.10

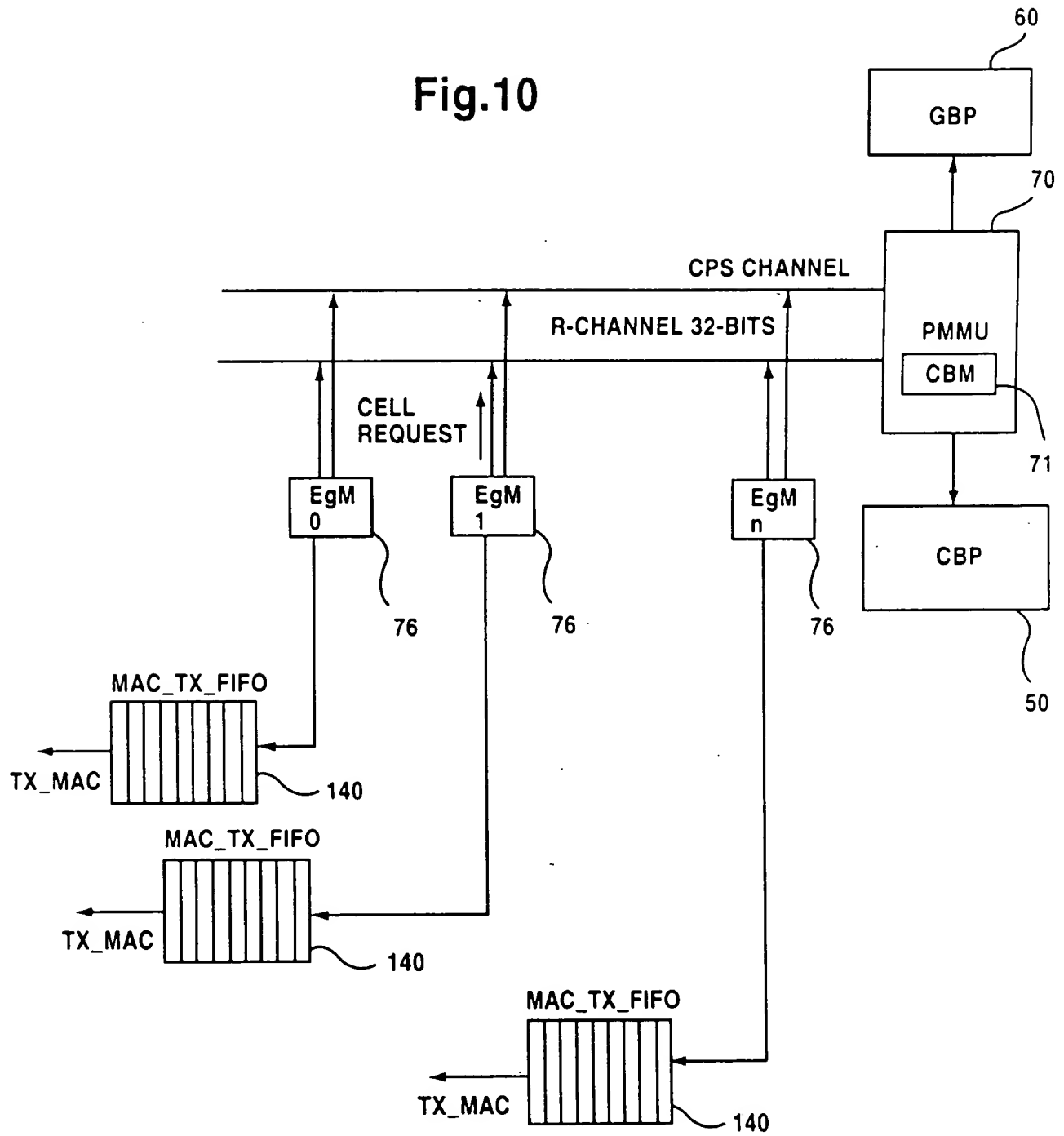


Fig.11

LINE 0 —→	FC LC BC/MC CPY_CNT(5b) CELL_LENGTH(7b _j) CRC(2b) NC_HEADER(16b) SRC_COUNT(6) IPX IP TIME_STAMP(14b) O BITS(2b) P NEXT_CELL_LEN(2b) CPU_OPCODE(4b) CELL_DATA(0-9B)
LINE 1 —→	CELL_DATA(10-27) BYTES
LINE 2 —→	CELL_DATA(28-45) BYTES
LINE 3 —→	CELL_DATA(46-63) BYTES

Fig.12

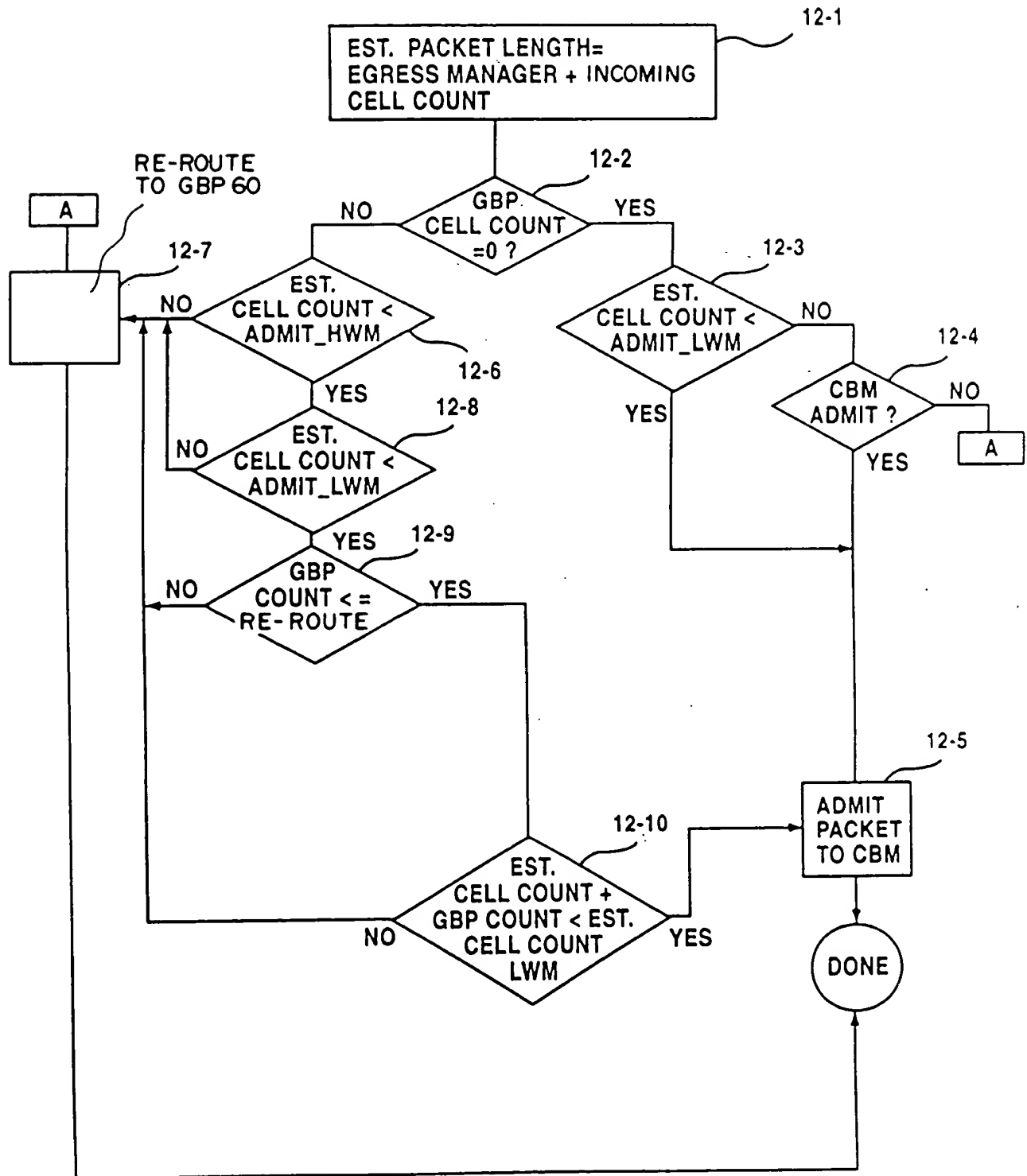
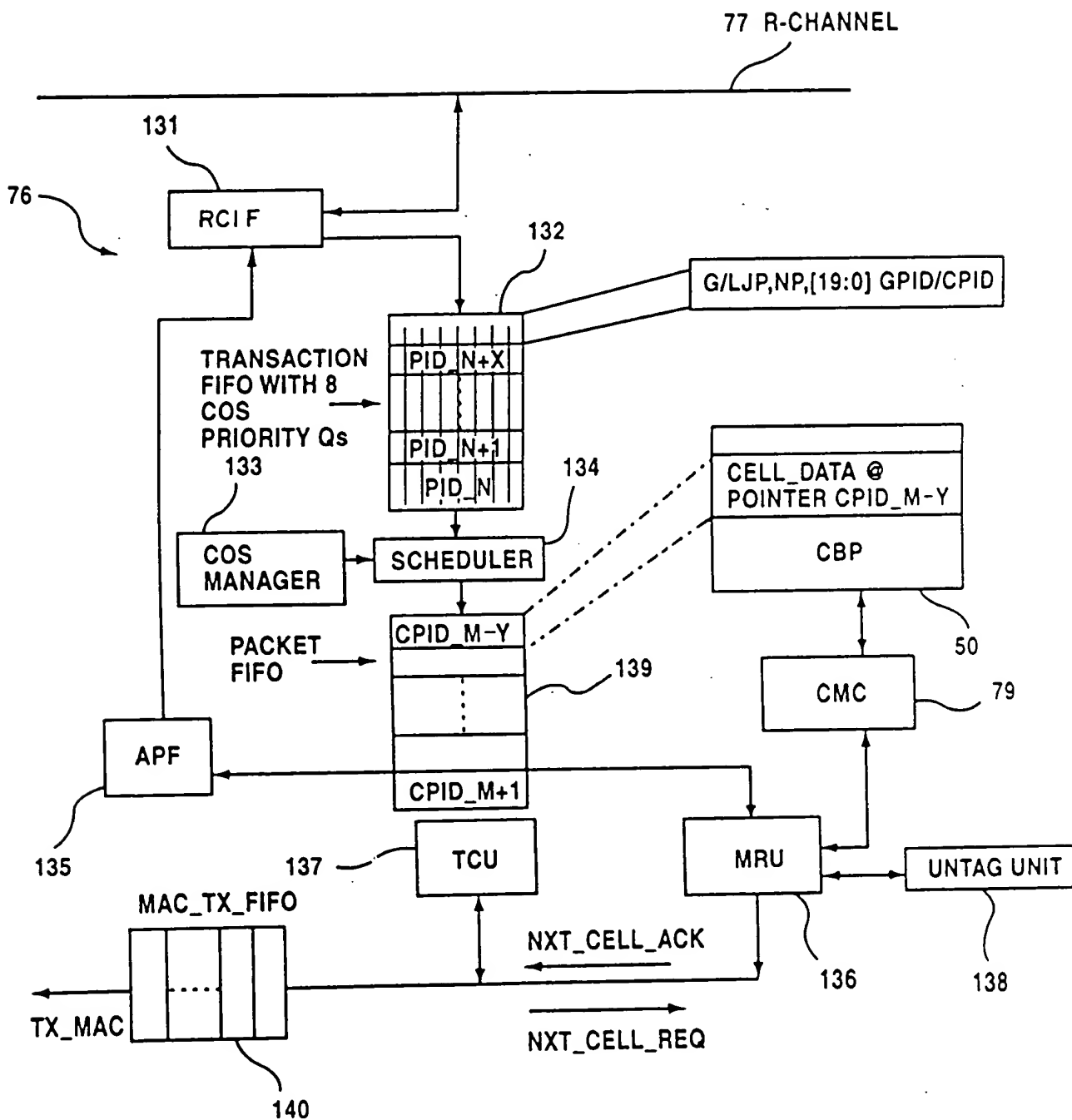


Fig.13



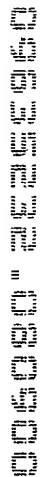
[illegible]

Fig.15

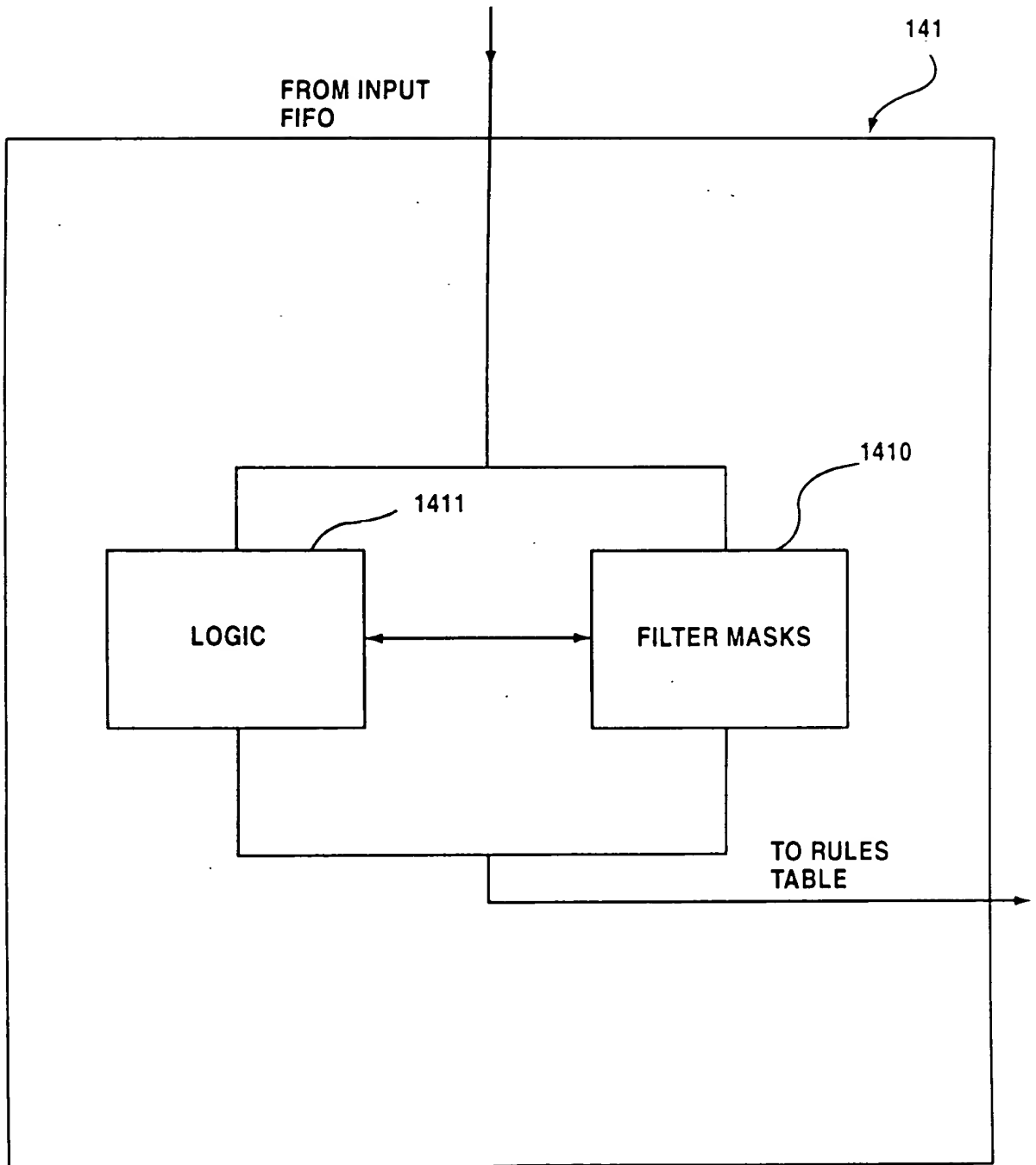


Fig.16

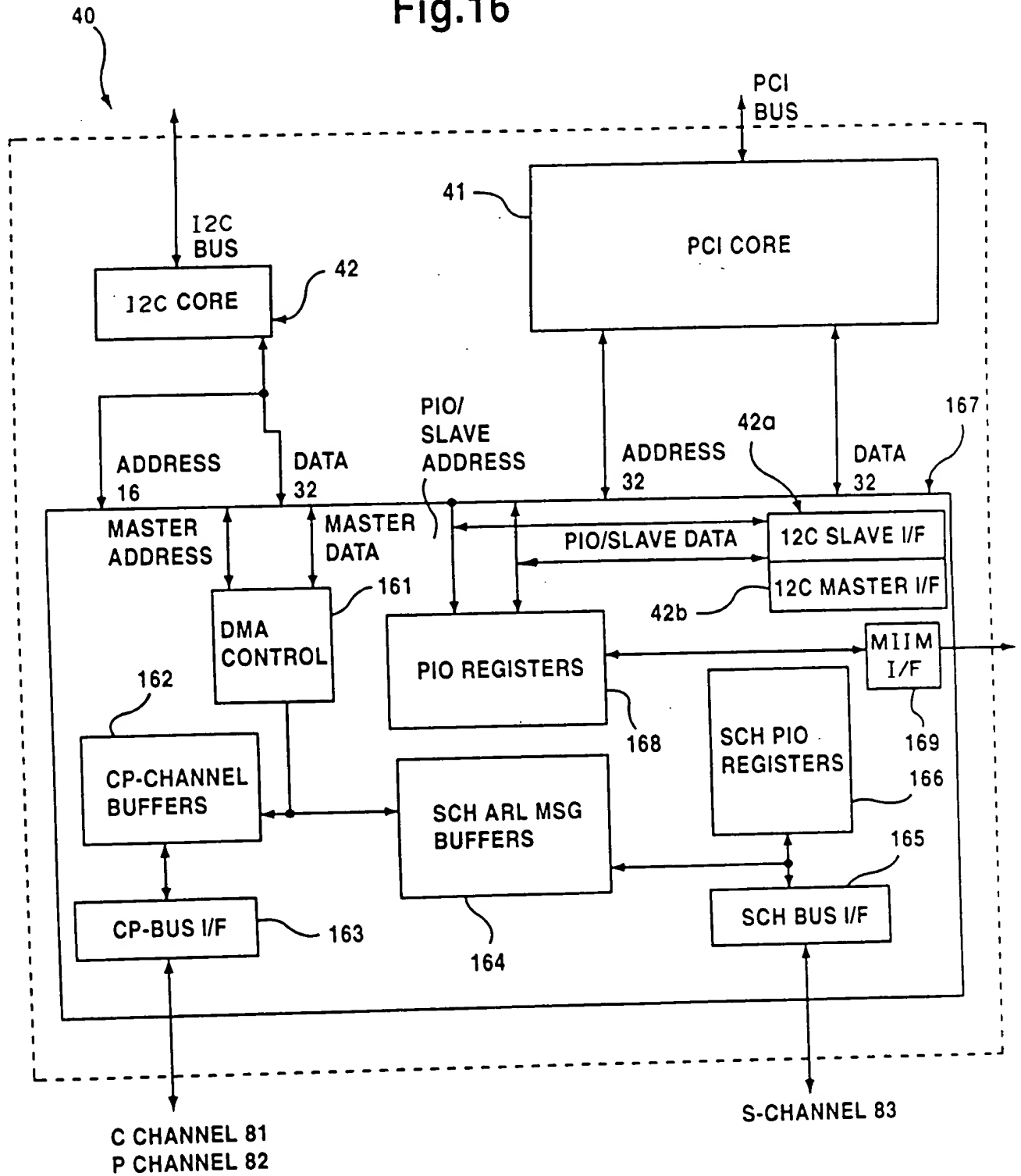


Fig.17

FFP PROGRAMMING FLOW CHART

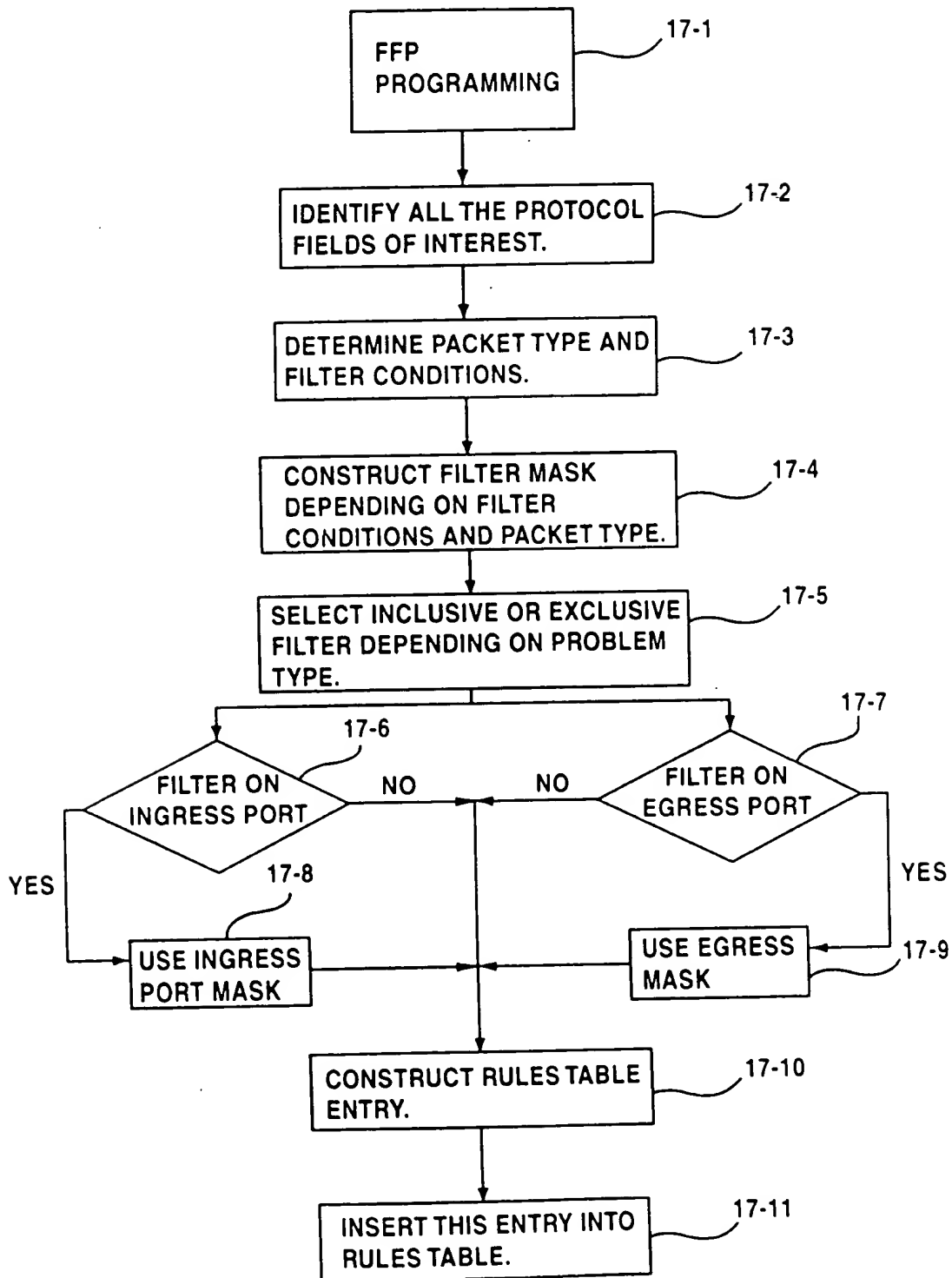


Fig.18

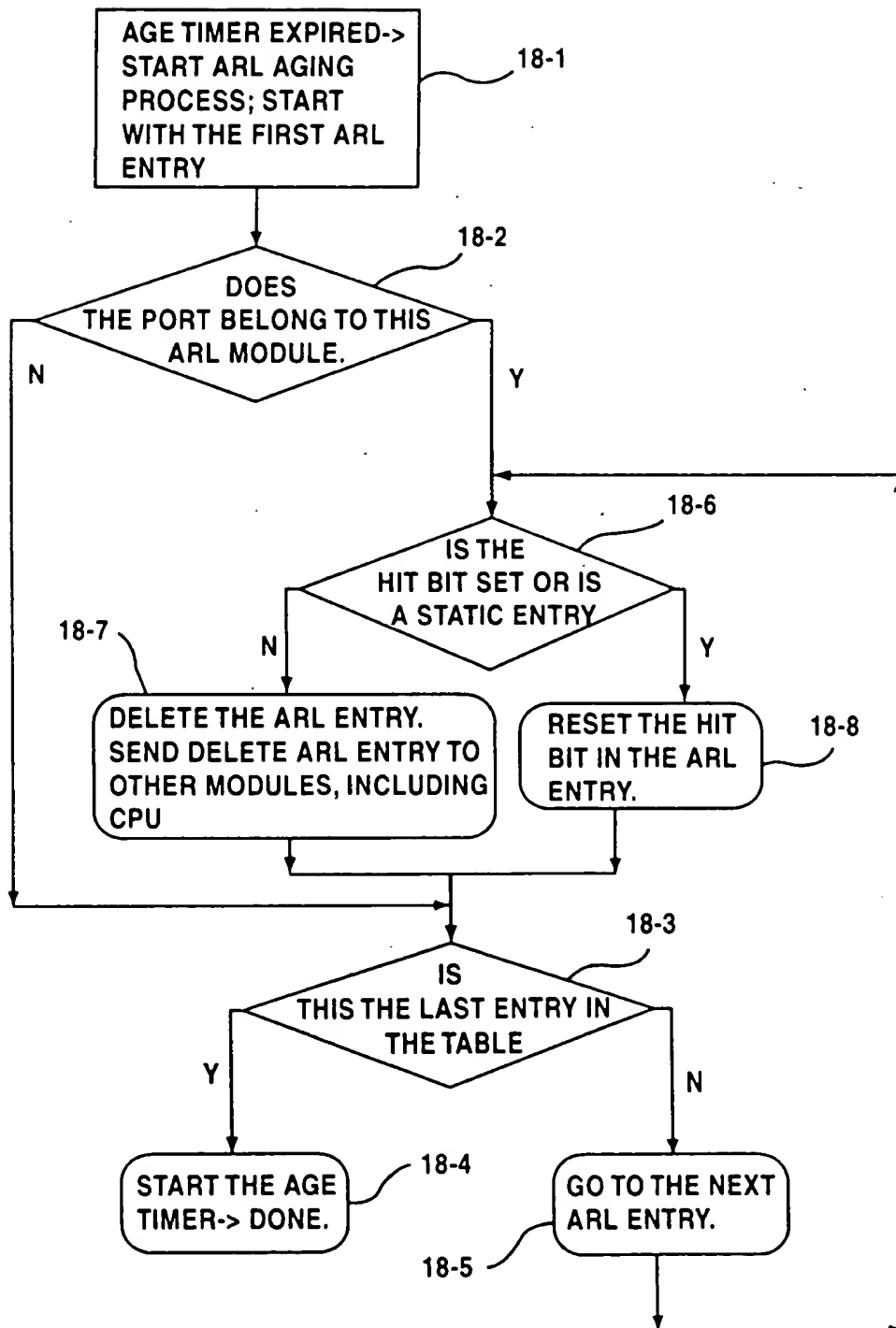


Fig.19

